

ABSTRACT OF THE DISCLOSUREMETHOD FOR PRODUCING AN ELECTRONIC COMPONENT WITH SELF-  
ALIGNED SOURCE, DRAIN AND GATE, IN DAMASCENE  
ARCHITECTURE

The invention concerns a method for fabricating an electronic component with self-aligned source, drain and gate, comprising the following steps:

- a) forming a dummy gate on a silicon substrate (100),
- b) forming a source (118) and drain (120) either side of the dummy gate,
- c) self-aligned, superficial siliciding of the source and drain,
- d) depositing at least one, so-called contact, metal layer (130, 132),
- e) replacing the dummy gate by at least one final gate (150).

Figure 9.